



Telephonic Speech Network

Overview

The SANYO LA8515N telephonic speech network provides amplification, switching and line drive functions for telephone equipment. It can perform 2 to 4 line conversion and impedance matching, and supports both DTMF and keytone signals.

The LA8515N's low operating current reduces line load. Switching between the DTMF/keytone and voice circuits is controlled directly from a single MUTE input.

The LA8515N is available in plastic 20-pin DIPs.

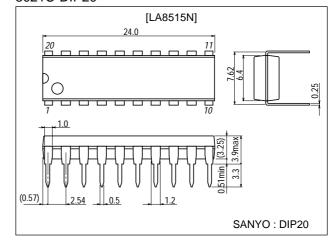
Features

- Direct connection to low-impedance receiver.
- DTMF/keytone and voice circuit switching controlled by a single MUTE input.
- Receive and transmit gain are adjusted automatically in response to the line current.
- Applicable to a wide variety of transmitters and receivers by selecting external components.

Package Dimensions

unit:mm

3021C-DIP20



Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Line voltage	V _L max		15	V
Line current	I _L max		150	mA
Allowable power dissipation	Pd max		1200	mW
Operating temperature	Topr		-30 to +75	°C
Storage temperature	Tstg		-55 to +150	°C

Operating Characteristics at $Ta = 25^{\circ}C$, f=1kHz, See specified Test Circuit.

Parameter	Symbol	Conditions	Ratings			Unit
r alametei	Symbol		min	typ	max	Oille
Line voltage	VL	I _L =20mA		3.6		V
		I _L =50mA		6.1		V
		I _L =120mA		12.3		V
Supply voltage	VCC VCC	I _L =20mA		2.1		V
		IL=50mA		3.6		V
		I _L =120mA		7.1		V

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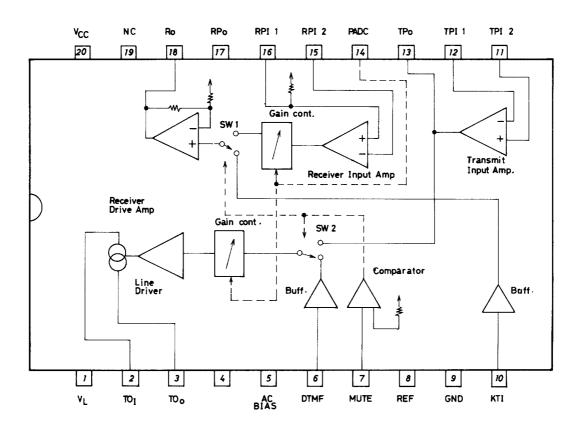
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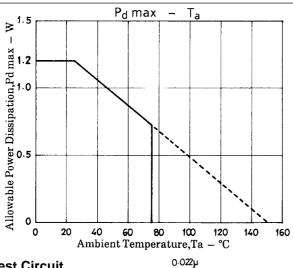
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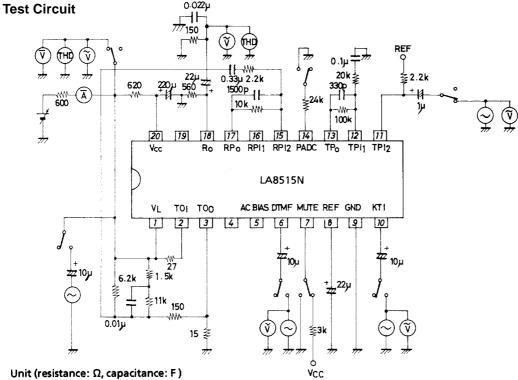
Parameter	Symbol	Conditions		Ratings		
Farameter			min	typ	max	Unit
Transmit gain	G _T	I _L =20mA, V _{IN} =-55dBV	38	40	42	dB
Transmit gain		I _L =120mA, V _{IN} =-55dBV	35	37		dB
Possive gain	G _R	I _L =20mA, V _{IN} =-20dBV	-4	-2	0	dB
Receive gain		I _L =120mA, V _{IN} =-20dBV	-9.5	-7	-5	dB
DTME gain		I _L =20mA, V _{IN} =-30dBV	23	25	27	dB
DTMF gain	G _{MF}	I _L =120mA, V _{IN} =-30dBV	20	22		dB
Transmit dynamic range	DR _T	I _L =20mA, THD=4%	2.5			Vp-p
Transmit dynamic range		I _L =120mA, THD=4%	4.6			Vp-p
Receive dynamic range	DR _R	I _L =20mA, THD=10%	0.3			Vp-p
Receive dynamic range		I _L =120mA, THD=10%	0.5			Vp-p
DTMF input impedance	Z _{IMF}	I _L =50mA	24			kΩ
KTI input impedance	Z _{KTI}	I _L =50mA	17			kΩ
MUTE high-level input voltage	V _{IH}	I _L =20mA to 120mA	1.5		Vcc	V
MUTE low-level input voltage	V _{IL}	I _L =20mA to 120mA	0		0.2	V
Transmit attenuation	ΔG _T	I _L =30mA, PADC grounded via 24kΩ		3		dB
Receive attenuation	∆G _R	I _L =30mA, PADC grounded via 24kΩ		6		dB
	VREF	I _L =20mA		0.65		V
Reference voltage		I _L =50mA		1.13		V
		I _L =120mA		2.1		V

Note) Be careful of dielectric breakdown.

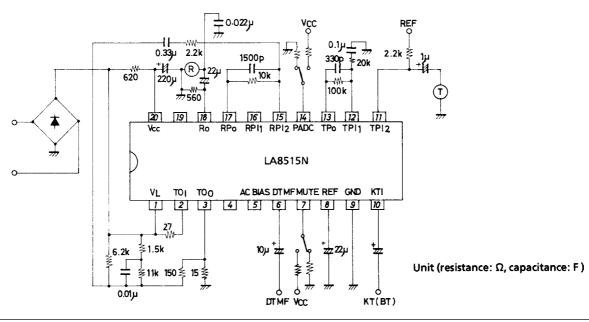
Equivalent Circuit Block Diagram





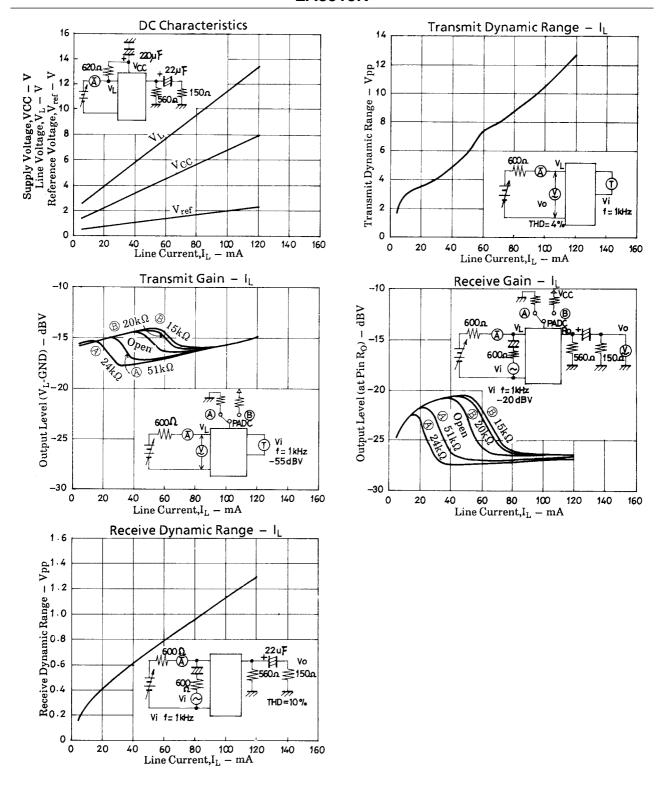


Sample Application Circuit



Pin Functions

Pin Number	Pin Name	Description
1	VL	Line voltage
		Connected to the positive side of the line diode bridge. See the application circuit.
2	TOI	Transmit output current source, input side
		Connected to V_L through a 27 Ω resistor. Select the value of this resistor after considering the maximum expected line
		current.
3	TO _O	Transmit output current source, output side
		As above, but to ground through a 15Ω resistor.
4		Not used.
		This pin has a DC bias and should not be connected.
5	AC BIAS	AC signal reference voltage
		An internally-generated reference voltage.
6	DTMF	DTMF input
		The signal on this pin is output on V _L (pin 1) when MUTE (pin 7) is LOW. It should be decoupled using a capacitor
		since it is biased with the REF voltage.
7	MUTE	Mute control input
		Switches between the transmit side DTMF or receive side keytone, and voice circuits.
		LOW : DTMF output, keytone receive output.
		HiGH: Voice circuits.
8	REF	Reference voltage
		Internal amplifier bias voltage. Requires an external capacitor. This voltage should not be used by external circuitry.
9	GND	Ground
		Connected to the negative side of the line diode bridge.
10	KTI	Key tone input
		Switched through to the receive circuit output when MUTE (pin 7) is LOW. It should be decoupled using a capacitor
		since it is biased with REF voltage.
11	TPI ₂	Transmit input amplifier non-inverting input
		Transmit voice circuit input. Requires a DC bias from REF (pin 8) through a resistor.
12	TPI ₁	Transmit input amplifier inverting input
		Negative feedback input. Amplifier gain and frequency response are controlled by the feedback network.
13	TPO	Transmit input amplifier output
14	PADC	Pad control input
		The value of the resistor between this pin and either V_{CC} or ground determines the shape of the line-current vs. gain
		characteristics. See Electrical Characteristics.
15	RPI ₂	Receive input amplifier inverting input
		Negative feedback is applied from the amplifier output to control amplifier gain and frequency response.
16	RPI ₁	Receive input amplifier inverting input
		This pin is internally biased through a resistor using REF.
17	RP _O	Receive input amplifier output
18	Ro	Receive circuit output
		Connect to a low-impedance (approximately 15kΩ) receiver through a decoupling capacitor.
19	NC	No connection
20	V _{CC}	Supply voltage
		Supply voltage for internal circuitry. This supply should not be used as an external circuit supply except as the high-
		level voltage for the MUTE and PADC inputs.



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