



Video Switch for TV/VCR Use

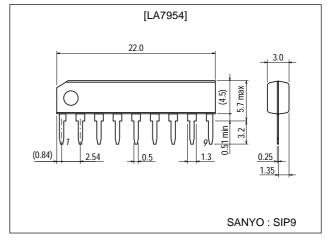
Features

- 4 inputs, 1 output.
- Excellent crosstalk characteristic.
- Wide band.

Package Dimensions

unit:mm

3017D-SIP9



Specifications

Maximum Ratings at Ta = 25°C

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Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V ₇ max		14	V
Maximum input supply voltage 1	V ₄ max, V ₆ max, V ₈ max, V ₉ max		8	V
Maximum input supply voltage 2	V ₂ max, V ₃ max	V _{CC} =14V	14	V
Maximum output current	I ₁ max		7	mA
Allowable power dissipation	Pd max	Ta≤65°C	540	mW
Operating temperature	Topr		-20 to +65	°C
Storage temperature	Tstg		-55 to +150	°C

Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Operating voltage range	V _{CC} op		8 to 13.5	V
Recommended supply voltage	Vcc		12	V

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Operating Characteristics at Ta = 25°C, $V_{CC}=12V$

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Quiescent current drain	lcc		9	12	17	mA
Input bias voltage	V ₄ , V ₆ , V ₈ , V ₉		4.5	4.8	5.1	V
Output bias voltage	V ₁		3.7	4.1	4.3	V
Output DC offset voltage	Vos		-50	0	+50	mV
Control threshold voltage	V _{2H} , V _{3H} ,		2.3			V
	V _{2L} , V _{3L}				0.7	V
Control input current	l ₂ , l ₃		-20	-6		μΑ
Voltage gain	GV	f=1MHz, V _{IN} =2Vp-p (Note 1)	-0.5	-0.2		dB
Frequency characteristic	GV-f	0dB at f=100kHz (Note 1) f=10MHz, V _{IN} =2Vp-p	-3	0		dB
Output dynamic range	V_{DR}	f=15kHz, V _{IN} =6Vp-p (Note 1)		0.3	6	%
Crosstalk (Note 2)		V _{IN} =2Vp-p, f=3MHz (Note 1)	48	58		dB
	C _T	V _{IN} =2Vp-p, f=5MHz (Note 1)	45	55		dB

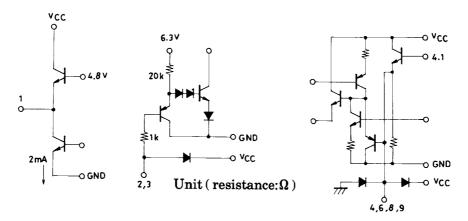
^{*} Current flowing into the IC is defined as positive; current flowing out is defined as negative.

Video Switch Truth Table

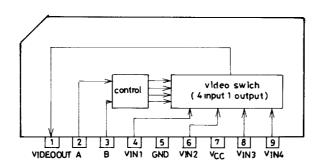
S2 (Pin 2)	S3 (Pin 3)	VIN1 (Pin 4)	V _{IN2} (Pin 6)	V _{IN3} (Pin 8)	V _{IN4} (Pin 9)
Н	Н	ON	OFF	OFF	OFF
L	Н	OFF	ON	OFF	OFF
Н	L	OFF	OFF	ON	OFF
L	L	OFF	OFF	OFF	ON

Note 1: Refer to this Truth Table and make measurements by switching S2, S3.

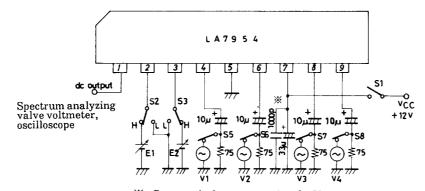
Input/Output Equivalent Circuit



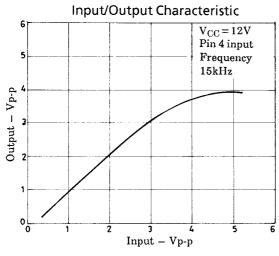
Equivalent Circuit Block Diagram

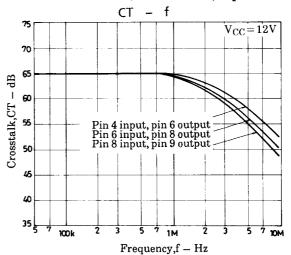


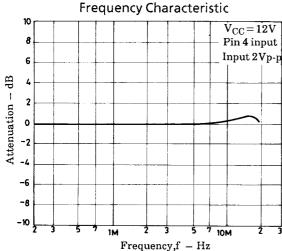
Test Circuit



% : Connect the bypass capacitor for V_{CC} as close to pin 7 as possible. $Unit \ (\ resistance: \Omega,\ capacitance: F\)$







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