

93L01 1-of-10 Decoder

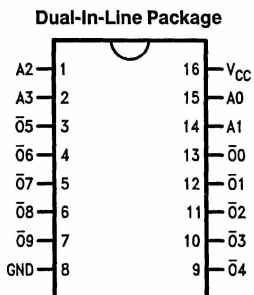
General Description

The 93L01 multipurpose decoders are designed to accept four inputs and provide ten mutually exclusive outputs.

Features

- Multifunction capability
- Mutually exclusive outputs
- Demultiplexing capability
- Typical power dissipation of 45 mW

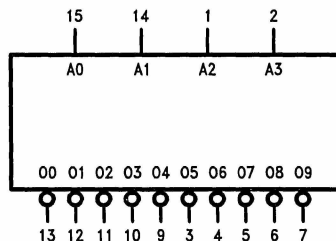
Connection Diagram



TL/F/9583-1

Order Number 93L01DMQB or 93L01FMQB
See Package Number J16A or W16A

Logic Symbol



V_{CC} = Pin 16
GND = Pin 8

TL/F/9583-2

Pin Names	Description
A0–A3	Address Inputs
$\bar{O}0$ – $\bar{O}9$	Decoder Outputs (Active LOW)

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
MIL	−55°C to +125°C
Storage Temperature Range	−65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	93L01 (MIL)			Units
		Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.7	V
I _{OH}	High Level Output Current			−400	μA
I _{OL}	Low Level Output Current			4.8	mA
T _A	Free Air Operating Temperature	−55		125	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = −10 mA			−1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max, V _{IL} = Max, V _{IH} = Min	2.4			V
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max, V _{IH} = Min, V _{IL} = Max			0.3	V
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 5.5V			1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.4V			20	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.3V			−400	μA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	−2.5		−25	mA
I _{CC}	Supply Current	V _{CC} = Max (Note 3)			13	mA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 3: I_{CC} is measured with all outputs open and all inputs grounded.

Switching Characteristics

V_{CC} = +5.0V, T_A = +25°C (See Section 3 for waveforms and load configurations)

Symbol	Parameter	C _L = 15 pF		Units
		Min	Max	
t _{PLH}	Propagation Delay An to On		36	ns
t _{PHL}			36	

Functional Description

The 93L01 decoder accepts four active HIGH BCD inputs and provides ten mutually exclusive active LOW outputs, as shown by logic symbol or diagram. The active LOW outputs facilitate addressing other MSI units with active LOW input enables. The logic design of the 93L01 ensures that all out-

puts are HIGH when binary codes greater than nine are applied to the inputs. The most significant input A3 produces a useful inhibit function when the 93L01 is used as a 1-of-8 decoder.

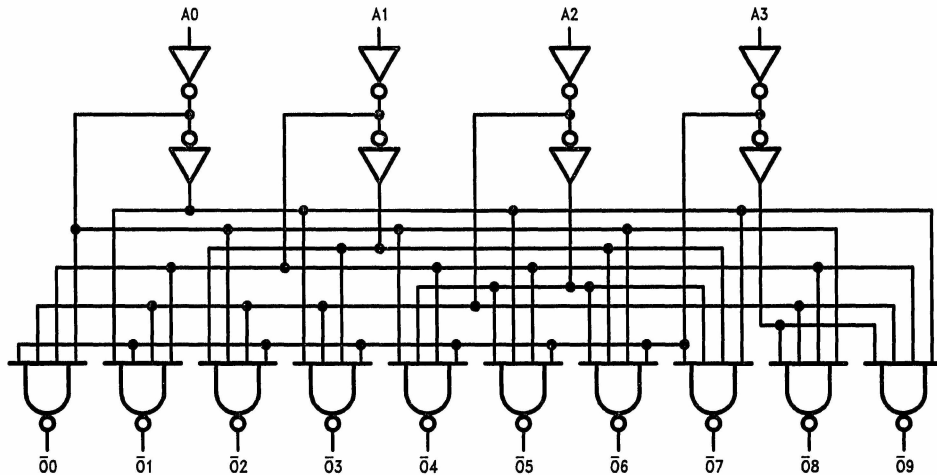
Truth Table

Inputs				Outputs									
A0	A1	A2	A3	$\bar{O}0$	$\bar{O}1$	$\bar{O}2$	$\bar{O}3$	$\bar{O}4$	$\bar{O}5$	$\bar{O}6$	$\bar{O}7$	$\bar{O}8$	$\bar{O}9$
L	L	L	L	L	H	H	H	H	H	H	H	H	H
H	L	L	L	H	L	H	H	H	H	H	H	H	H
L	H	L	L	H	H	L	H	H	H	H	H	H	H
H	H	L	L	H	H	H	L	H	H	H	H	H	H
L	L	H	L	H	H	H	H	L	H	H	H	H	H
H	L	H	L	H	H	H	H	H	L	H	H	H	H
L	H	H	L	H	H	H	H	H	H	L	H	H	H
H	H	H	L	H	H	H	H	H	H	H	L	H	H
L	L	L	H	H	H	H	H	H	H	H	H	L	H
H	L	L	H	H	H	H	H	H	H	H	H	H	L
L	H	L	H	H	H	H	H	H	H	H	H	H	H
H	H	L	H	H	H	H	H	H	H	H	H	H	H
L	L	H	H	H	H	H	H	H	H	H	H	H	H
H	L	H	H	H	H	H	H	H	H	H	H	H	H
L	H	H	H	H	H	H	H	H	H	H	H	H	H
H	H	H	H	H	H	H	H	H	H	H	H	H	H

H = HIGH Voltage Level

L = LOW Voltage Level

Logic Diagram



TL/F/9583-3