

0°C to +75°C

DIGITAL 8000 SERIES TTL MSI

DESCRIPTION

The 8T23 is a Dual Line Driver designed to meet all of the requirements of the IBM System/360 I/O Interface Specification for interface drivers.

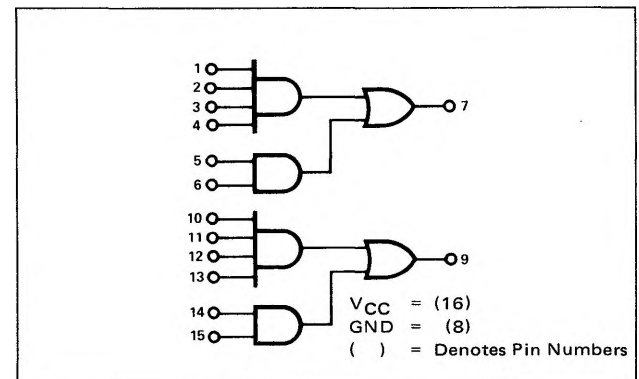
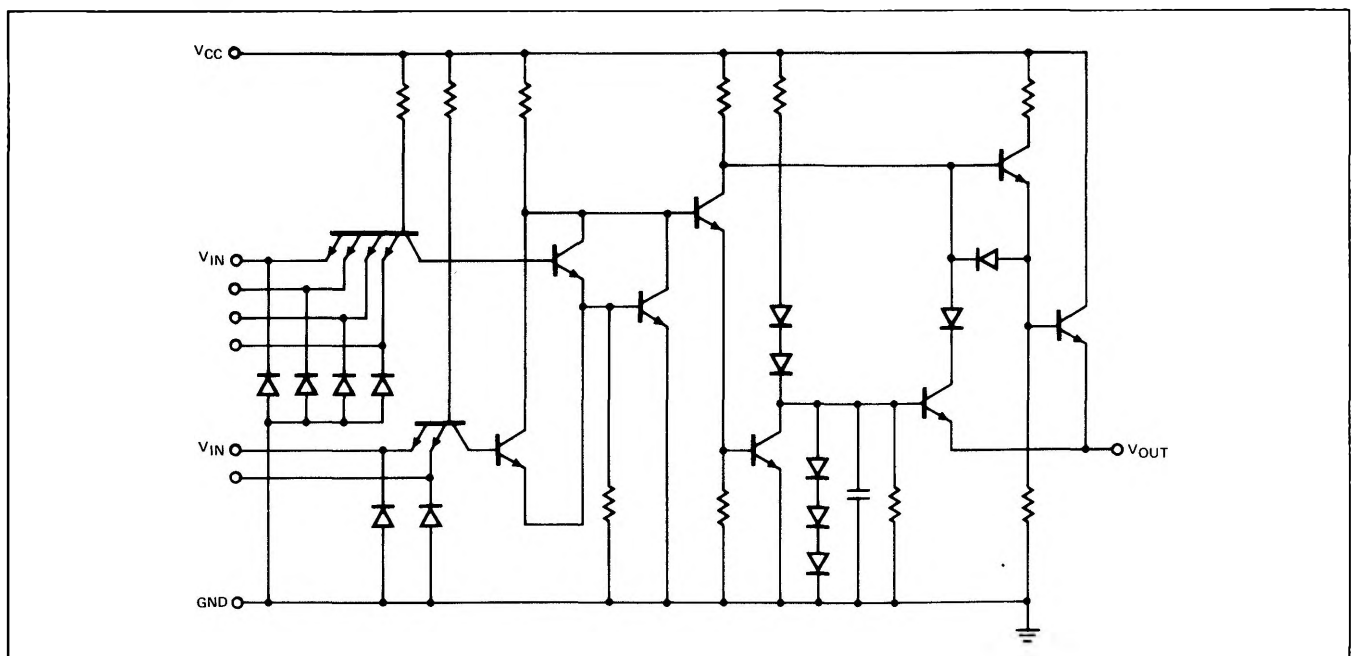
The low impedance emitter follower output will drive terminated lines such as coaxial cable or twisted pair. The output is protected against accidental shorting by an internal clamping network which turns on once the output voltage drops below approximately 1.5 volts. The uncommitted emitter output structure allows Dot-OR logic to be performed as in "Party-Line" operations.

Multiple emitter inputs allow the 8T23 to interface with standard TTL or DTL systems and the circuit operates from a single +5 volt power supply.

Additional logic incorporated in the 8T23 Dual Line Driver can be used during the power-up and power-down sequence to ensure that no spurious noise is generated on the line.

FEATURES

- $I_{OUT} = 59.3\text{mA}$ AT 3.11 VOLTS
- UNCOMMITTED EMITTER OUTPUT STRUCTURE FOR PARTY-LINE OPERATION
- SHORT-CIRCUIT PROTECTION
- SINGLE 5 VOLT POWER SUPPLY
- AND-OR LOGIC CONFIGURATION

LOGIC DIAGRAM WITH PIN LAYOUT**CIRCUIT SCHEMATIC**

DIGITAL 8000 SERIES TTL/MSI – 8T23

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0V \pm 5\%$, $T_A = 0^\circ C$ TO $+75^\circ C$)

CHARACTERISITCS	LIMITS				TEST CONDITIONS				NOTES
	MIN	TYP	MAX	UNITS	AND GATE # 1		INPUTS OF # 2 AND GATE	OUTPUT	
					INPUT UNDER TEST	OTHER INPUT			
"0" Output Voltage			+0.15	V	0.8V	4.5V	0V	-240μA	7
"1" Output Leakage Current			40	μA	0V	0V	0V	3.0V	1, 15
"0" Input Current	-0.1		-1.6	mA	0.4V	4.5V			
"1" Input Current			40	μA	4.5V	0V			

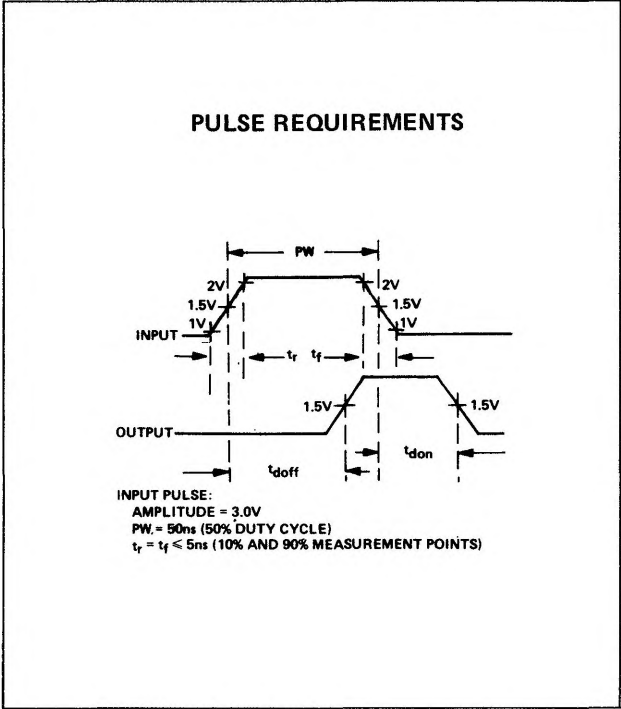
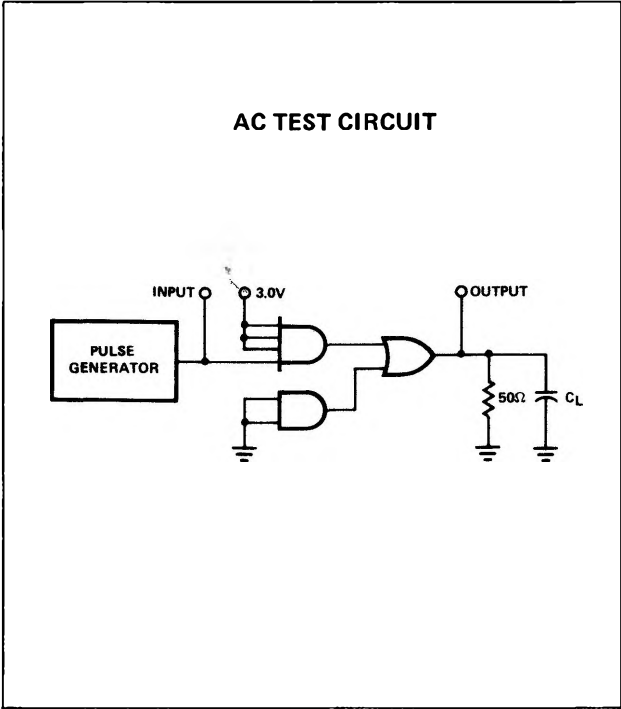
ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0V$, $T_A = 25^\circ C$)

CHARACTERISTICS	LIMITS				TEST CONDITIONS				NOTES
	MIN	TYP	MAX	UNITS	AND GATE # 1		INPUTS OF # 2 AND GATE	OUTPUT	
					INPUT UNDER TEST	OTHER INPUT			
"1" Output Voltage	3.11			V	2.0V	2.0V	0.8V	59.3mA	
Turn-on Delay		12 15	20 25	nS nS					8, 13 9, 13
Turn-off Delay		12 20	20 35	nS nS					8, 13 9, 13
Power/Current Consumption Output at "0"			315/ 60	mW/ mA	0.8V	0.8V	0.8V		12, 16
Output at "1"			150/ 28	mW/ mA	2.0V	2.0V	2.0V		12, 16
Input Latch Voltage	5.5			V	10mA	0V	0V		11
"1" Output Current	-100		-250	mA	4.5V	4.5V	0V	2.0V	14
Input Clamp Voltage			-1.5	V	-12mA				14

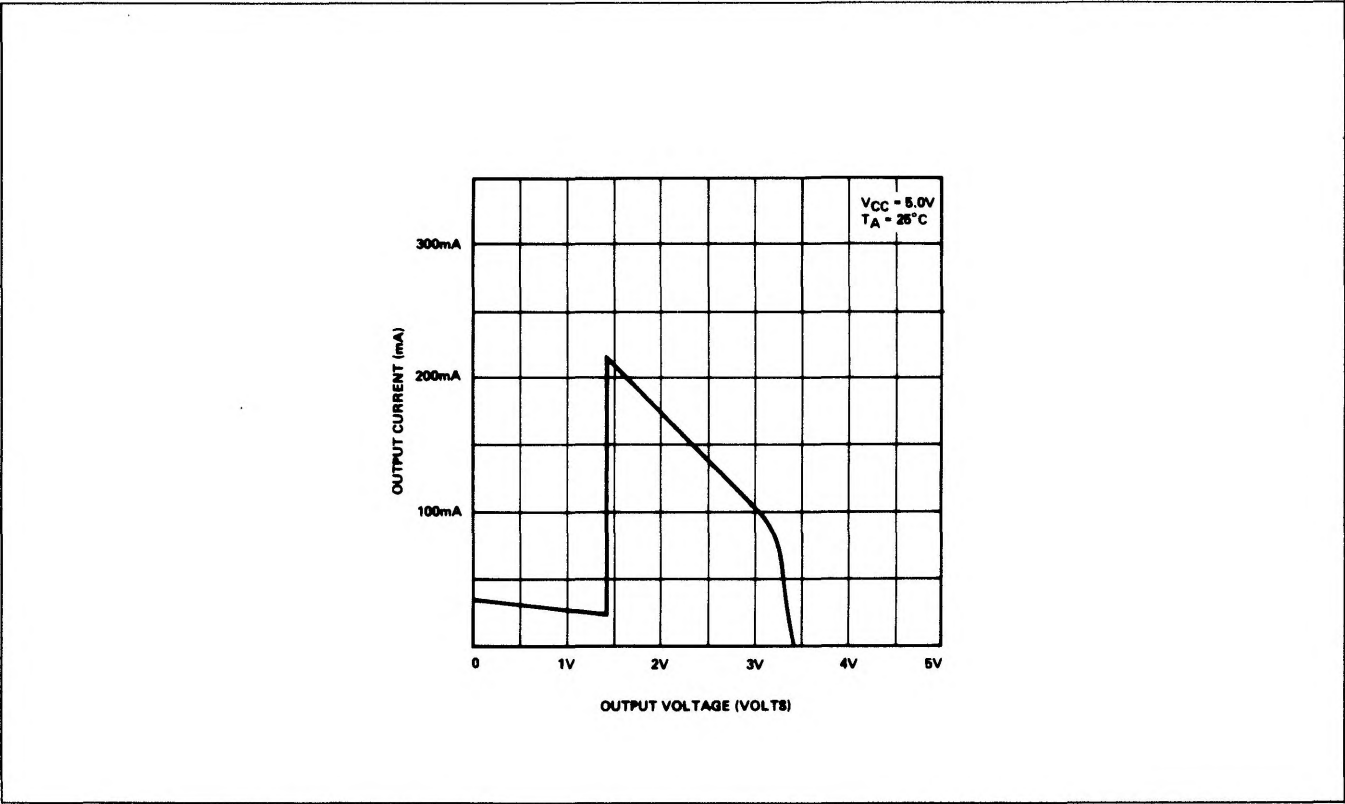
NOTES:

1. All voltage measurements are referenced to the ground terminal. Terminals not specifically referenced are left electrically open.
2. All measurements are taken with ground pin tied to zero volts.
3. Positive current is defined as into the terminal referenced.
4. Positive logic definition: "UP" Level = "1", "DOWN" Level = "0".
5. Precautionary measures should be taken to ensure current limiting in accordance with Absolute Maximum Ratings should the isolation diodes become forward biased.
6. Output source current is supplied through a resistor to ground.
7. With forced output current of 240 μ A the output voltage must not exceed 0.15V.
8. $R_L = 50\Omega$ to ground.
9. Load is 50Ω in parallel with 100pF.
10. Manufacturer reserves the right to make design and process changes and improvements.
11. This test guarantees operation free of input latch-up over the specified operating supply voltage range.
12. I_{CC} is dependent upon loading. I_{CC} limit specified is for no-load test condition for both drivers.
13. Reference AC Test Circuit and Pulse Requirements.
14. Reference "Typical Output Current vs. Output Voltage Curve".
15. $V_{CC} = 0.00V$.
16. $V_{CC} = 5.25V$.

AC TEST FIGURE AND WAVEFORMS



TYPICAL OUTPUT CHARACTERISTICS



TYPICAL APPLICATIONS

