

OBJECTIVE SPECIFICATION

82S290-F,N • 82S291-F,N

DESCRIPTION

The 82S290 and 82S291 include on-chip decoding and 3 programmable chip enable inputs for ease of memory expansion. They feature either open collector or tri-state outputs for optimization of word expansion in bused organizations.

Both 82S290 and 82S291 devices are available in the commercial and military temperature ranges. For the commercial temperature range (0°C to $+75^{\circ}\text{C}$) specify N82S290/291, F or N, and for the military temperature range (-55°C to $+125^{\circ}\text{C}$) specify S82S290/291, F.

FEATURES

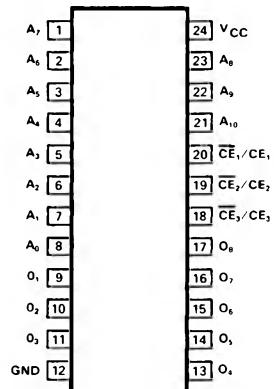
- Address access time:
N82S290/291: 80ns max
S82S290/291: 100ns max
- Power dissipation: 40 $\mu\text{W}/\text{bit typ}$
- Input loading:
N82S290/291: $-100\mu\text{A}$ max
S82S290/291: $-150\mu\text{A}$ max
- On-chip address decoding
- Output options:
82S290: Open collector
82S291: Tri-state
- Fully compatible with Signetics 82S190/191 PROMs
- Fully TTL compatible

APPLICATIONS

- Sequential controllers
- Microprogramming
- Hardwired algorithms
- Control store
- Random logic
- Code conversion

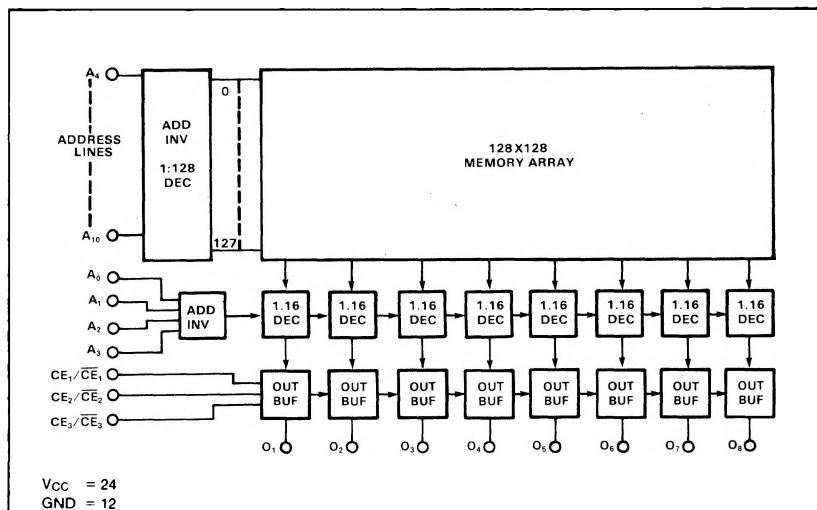
PIN CONFIGURATION

F,N PACKAGE*



*F = Cerdip
N = Plastic

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

| PARAMETER | RATING | UNIT |
|------------------|---|----------------------------|
| V _{CC} | Supply voltage | Vdc |
| V _{IN} | Input voltage | Vdc |
| | Output voltage | Vdc |
| V _{OH} | High (82S290) | +5.5 |
| V _O | Off-state (82S291) | +5.5 |
| T _A | Temperature range | °C |
| | Operating N82S290/291 S82S290/291 | 0 to +75 |
| T _{STG} | Storage | -55 to +125 -65 to +150 |

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DC ELECTRICAL CHARACTERISTICS N82S290/291: $0^\circ\text{C} \leq T_A \leq +75^\circ\text{C}$, $4.75\text{V} \leq V_{CC} \leq 5.25\text{V}$
S82S290/291: $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$, $4.5\text{V} \leq V_{CC} \leq 5.5\text{V}$

| PARAMETER | TEST CONDITIONS ¹ | N82S290/291 | | | S82S290/291 | | | UNIT |
|---|---|--|---|------|-------------|------------------|------|---------------|
| | | Min | Typ ² | Max | Min | Typ ² | Max | |
| V _{IL} V _{IH} V _{IC} | Input voltage Low High Clamp | | | | .85 | | .80 | V |
| V _{OL} V _{OH} | Output voltage Low High (82S291) | I _{IN} = -18mA | 2.0 | -0.8 | -1.2 | 2.0 | -0.8 | -1.2 |
| I _{IL} I _{IH} | Input current Low High | I _{OUT} = 9.6mA \overline{CE} = Low, I _{OUT} = -2.4mA, High stored | 2.4 | | 0.45 | 2.4 | | 0.5 |
| I _{OLK} I _{O(OFF)} | Output current Leakage (82S290) Hi-Z state (82S291) | \overline{CE} = High, V _{OUT} = 5.5V \overline{CE} = High, V _{OUT} = 0.5V \overline{CE} = High, V _{OUT} = 5.5V V _{OUT} = 0V | | | 40 | | | μA |
| I _{OS} | Short circuit (82S291) | | | -100 | 40 | | | 60 |
| I _{CC} | V _{CC} supply current | | | 40 | -40 | | | -60 |
| C _{IN} C _{OUT} | Capacitance Input Output | V _{CC} = 5.0V V _{IN} = 2.0V V _{OUT} = 2.0V | V _{IN} = 0.45V V _{IN} = 5.5V | 5 | 8 | | | 60 |
| | | | | -70 | -15 | | | 80 |
| | | | | | | 130 | 180 | mA |
| | | | | | | 170 | | pF |
| | | | | | | | 5 | 8 |

AC ELECTRICAL CHARACTERISTICS

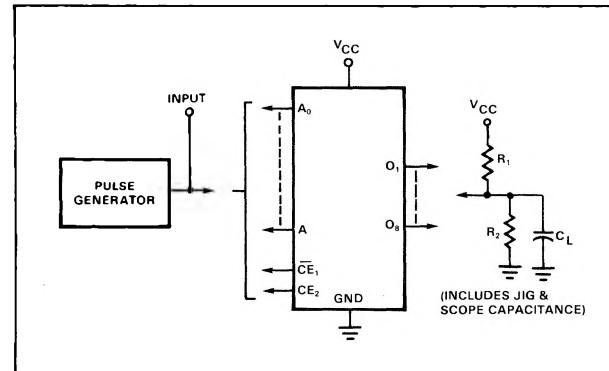
 $R_1 = 270\Omega$, $R_2 = 600\Omega$, $C_L = 30\text{pF}$ N82S290/291: $0^\circ\text{C} \leq T_A \leq +75^\circ\text{C}$, $4.75\text{V} \leq V_{CC} \leq 5.25\text{V}$ S82S290/291: $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$, $4.5\text{V} \leq V_{CC} \leq 5.5\text{V}$

| PARAMETER | TO | FROM | N82S290/291 | | | S82S290/291 | | | UNIT |
|------------------------------------|--------------|------------------|------------------------|------------------|----------|-------------|------------------|-----------|------|
| | | | Min | Typ ² | Max | Min | Typ ² | Max | |
| T _{AA} T _{CE} | Access time | Output Output | Address Chip enable | 50 20 | 80 40 | | 50 20 | 100 50 | ns |
| T _{CD} | Disable time | Output | Chip disable | | 20 | 40 | | 20 | 50 |

NOTES

- Positive current is defined as into the terminal referenced.
- Typical values are at $V_{CC} = 5.0\text{V}$, $T_A = +25^\circ\text{C}$.

TEST LOAD CIRCUIT



VOLTAGE WAVEFORM

